		EAST Searc	h History	ام 14) ا	: Spr	• •
Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1684	switched adj terminal	US-PGPUB; USPÁT; EPO; JPO; DERWENT; IBM_TDB	OR .	ON	2007/03/05 08:46
L2	25	switched adj terminal.ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON ,.	2007/03/05 08:48
L3	. 0	switched adj terminal and substrate and ("N well" N\$1well) and (regulat\$3 stability stabilization stablizing) near3 (substrate near2 (potential voltage))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/05 08:50
L4		switched adj terminal and substrate and (cmos cmosfet) and (regulat\$3 stability stabilization stablizing) near3 (substrate near2 (potential voltage))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/05 08:50
L5	0	switched adj terminal and substrate and (regulat\$3 stability stabilization stablizing) near3 (substrate near2 (potential voltage))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/05 08:51
L6	0	switched adj terminal and substrate and (regulat\$3 stability stabilization stabilizing) near3 (substrate near2 (potential voltage))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/05 08:51
L7	167	switched adj terminal and substrate	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/05 08:52
L8	114	switched adj terminal and substrate and @ad<"20031113"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/05 08:53
L9	114	"switched terminal" and substrate and @ad<"20031113"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/05 08:54

L10	90	"switched terminal" and substrate and @ad<"20031113"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/05 08:55
L11	52	"switched terminal" and substrate and @ad<"20031113" and (semiconductor silicon)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/05 09:08
L12	20	"switched terminal" and substrate and @ad<"20031113" and (semiconductor silicon) and (mosfet mos cmos cmosfet)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR .	OFF	2007/03/05 09:11
L13	. 561	(257/173).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/05 09:11
L14	233	13 and esd	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/05 09:11
L15	195	13 and esd	USPAT	OR.	OFF	2007/03/05 09:12
L16	. 0	13 and esd and switched adj terminal	USPAT	OR	OFF	2007/03/05 09:12
L17	56	13 and esd and (switch switching switched)	USPAT	OR	ON	2007/03/05 09:27
L18	76	transmeta.as.	USPAT	OR	ON	2007/03/05 09:27
L19	4	transmeta.as. and switch\$3 and terminal and substrate	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/05 09:47
L20		control adj circuit near2 coupled near2 ground and 257/173.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/05 09:48

L21	1	control adj circuit near4 coupl\$3 near4 ground and 257/173.ccls.	US-PGPUB; USPAT; USOCR;	OR	ON	2007/03/05 10:30
			FPRS; EPO; JPO; DERWENT; IBM_TDB			
L22	3328	((327/534) or (327/535) or (327/536) or (327/327) or (257/299) or (257/901)).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/05 10:31
L23	4	22 and switch\$3 near1 terminal and substrate and (bias\$3 adj supply body adj bias\$3) and ground	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON 	2007/03/05 10:33
L24	0	((fu near1 robert).in. (chen near1 (tien-min)).in. transmeta.as.) and switch\$3 near1 terminal.clm. and substrate.clm. and (bias\$3 adj supply body adj bias\$3).clm. and ground.clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR .	ON	2007/03/05 10:34
S1	5	"712523".ap.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/05 08:21
S2	5	"712523".ap.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/24 09:52
S3	10	integrated adj circuit near3 switch. clm. and integrated adj circuit near3 switch.ti. and "257"/\$9.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/24 09:54
S4 :	<b>11</b>	integrated adj circuit near3 switch. ab,clm. and integrated adj circuit near3 switch.ti. and "257"/\$9.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/24 09:58
S5	585	spst adj switch	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/24 09:58

S6	74	spst adj switch.ti,ab,clm.	US-PGPUB; USPAT; EPO; JPO;	OR	OFF	2005/01/24 09:58
•	•		DERWENT; IBM_TDB		:	
S7	Ö	spst adj switch.ti,ab,clm. and "257"/\$9.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/24 09:59
S8	11	spst adj switch and "257"/\$9.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/24 10:03
S9	.5	spst adj switch near10 (cmos cmosfet)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF _	2005/01/24 10:46
S10	10	cmos adj switch.ti,ab,clm. and "257"/\$9.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/24 10:04
S11	11	cmos adj switch.ti,ab,clm. and "257"/\$9.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/24 10:38
S12	17	comfet.ti,ab,clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/24 10:08
S13	0	regenerative adj switch near4 (mosfet cmos cmosfet)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF .	2005/01/24 10:30
S14		regenerative adj switch near4 spst	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/24 10:30
S15	<b>o</b> :	regenarive adj switch and spst	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	1	ON	2005/01/24 10:38

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S16	0	regenerative adj switch and spst	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/24 10:42
S17	663	regenerative near10 (single spst)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/24 10:43
S18	0	regenerative near4 spst	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/24 10:53
519	0	regenerative near10 spst	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/24_10:44
S20	0	spst adj switch near10 (cmos cmosfet) and "257"/\$9.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/24 10:52
S21	0	spst adj switch.ti,ab,clm. and "257"/\$9.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/24 10:52
S22	15	(cmos cmosfet).ti,ab,clm. and switch near4 substrate near4 potential	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	· ·	ON	2005/01/24 10:54
S23	5	"712523".ap.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/11 12:01
S24	50	(spdt single-pole adj double-throw) adj switch.ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/11 13:05
S25	5	(spdt single-pole adj double-throw) adj switch.ti. and (spdt single-pole adj double-throw) adj switch.clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR .	ON	2005/02/11 13:12

S26	0	(spdt single-pole adj double-throw) adj switch.ti. and 257/109.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/11 14:15
S27	20	regenerative adj2 switch.ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/11 13:21
S28	130	spdt adj switch and (cmosfet cmos nfet pfet nmosfet pmosfet)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/11 13:22
S29		(US-20040022025-\$ or US-20030222704-\$ or US-20030042567-\$).did. or (US-6836172-\$ or US-5818099-\$). did.	US-PGPUB; USPAT	OR .	OFF	2005/02/11 13:41
S30	54	(257/548).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR ·	OFF	2005/02/11 13:42
S31	0	(spdt single-pole adj double-throw) adj switch and 257/109.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/11 14:15
S32	144	257/109.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/11 14:18
S33	130	(257/299).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/11 14:18
S34	2	S33 and variable near6 bias and low near3 voltage	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/11 14:19
: S35		S33 and low near3 (voltage potential)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/11 15:11

S36	8	(US-20040022025-\$ or US-20030222704-\$ or	US-PGPUB; USPAT	OR	OFF	2005/02/11 15:41
	•	US-20030042567-\$).did. or (US-6836172-\$ or US-5818099-\$ or US-5488247-\$ or US-6303957-\$ or US-6218895-\$).did.			1	
S37	5	"712523".ap.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/23 11:52
S38	1076	spdt adj switch	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/23 12:19
539	3	spdt adj switch and substrate and (cmos cmosfet) and charge adj pump\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/23 12:20
S40	3	spdt adj switch and substrate and (cmos cmosfet) and charge adj pump\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/23 12:22
S41	20	(single adj pole adj double adj throw spdt) adj switch and substrate and charge adj pump\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/23 12:25
S42	9	(single adj pole adj double adj throw spdt) adj switch and substrate and charge adj pump\$3 near6 switch\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/23 12:29
S43	4	(single adj pole adj double adj throw spdt) adj switch and substrate and charge adj pump\$3 near6 switch\$3 and current near4 (leak leakage leaking)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR .	ON	2005/05/23 12:31
S44	2	(single adj pole adj double adj throw spdt) adj switch and substrate and charge adj pump\$3 near6 switch\$3 and current near4 (leak leakage leaking) and SPDT	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/23 12:32

S45	4	(single adj pole adj double adj throw) adj switch and substrate and charge adj pump\$3 near6 switch\$3 and current near4 (leak leakage leaking)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/23 12:45
S46	4	(single adj pole adj double adj throw) adj switch and substrate and charge adj pump\$3 near6 switch\$3 and current near4 (leak leakage leaking) and (grnd ground grounded)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/23 13:01
S47	4	(single adj pole adj double adj throw) adj switch and substrate and charge adj pump\$3 near6 switch\$3 and current near4 (leak leakage leaking) and (grnd ground grounded) and (input output)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/23 13:56
S48	1	(single adj pole adj double adj throw) adj switch and substrate and charge adj pump\$3 near6 switch\$3 and current near4 (leak leakage leaking) and (grnd ground grounded) and (input output) and substrate near4 (potential voltage)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON :	2005/05/23 13:16
S49	1	switch near6 regulate near4 substrate near1 potential	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON .	2005/05/23 13:57
S50	6	switch near6 (regulating regulate) near4 substrate near1 (bias voltage potential)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/23 14:22
, S51	136	(257/299).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR -	OFF .	2005/05/23 14:27
S52	1	S51 and (switch\$3 near6 (bias biasing) near6 ground)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/23 16:24
S53	103	(cmos cmosfet).ti,ab,clm. and plurality near3 wells	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ÖFF	2005/05/23 17:10

F		(C-1) N   1   1		OD	055	2005/05/22 17:12
, S54 	1	(cmos cmosfet).ti,ab,clm. and plurality near3 wells and nakazato. in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/23 17:10
S55	. 11	(US-20030042567-\$ or US-20030151877-\$ or US-20030222704-\$ or US-20040022025-\$).did. or (US-4798974-\$ or US-5386135-\$ or US-5488247-\$ or US-5818099-\$ or US-6218895-\$ or US-6303957-\$ or US-6836172-\$).did.	US-PGPUB; USPAT	OR .	OFF	2005/05/24 08:13
S56	141	(257/901).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/24 08:33
S57	3	S56 and (switch switching) and charge adj (pump pumping enabl\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/24 09:13
S58	595	(327/534).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR .	OFF	2005/05/24 09:13
S59	28	S58 and substrate near2 bias\$3 near20 switch\$3 and charge adj pump\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/24 09:14
; ; S60	23	S58 and substrate near2 bias\$3 near20 switch\$3 and charge adj pump\$3 and (cmos cmosfet)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/24 11:26
S61	2853	((327/534) or (327/535) or (327/536) or (327/537) or (257/299) or (257/901)).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/05/24 11:26
S62	8	S61 and charge adj pump and substrate near3 bias and "p-type" near2 substrate and "n-well" near4 bias and (switch switching)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR .	OFF	2005/05/24 11:28

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: S63	5	"712523".ap.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR ·	OFF	2005/11/06 13:21
S64	2	("4798974").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/06 17:15
S65	0	"control adj input".ti,ab,clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/06 17:16
S66	. 0	"control adj input"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/06 17:16
S67	0	"control adj line"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/06 17:17
S69	55195	"control input".ti,ab,clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/06 17:17
S70	1229	"control input".ti,ab,clm. and supply adj line	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/06 17:17
S71	166	"control input".ti,ab,clm. and supply adj line and substrate	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/06 17:18
S72	166	"control input".ti,ab,clm. and supply adj line and substrate and control adj input	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/06 18:58
<b>573</b>	5079	charge adj pump.ti,ab,clm. and capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/06 18:58

S74	1816	charge adj pump near2 capacitor	US-PGPUB; USPAT; EPO; JPO;	OR	ON	2005/11/06 18:58
		•	DERWENT; IBM_TDB			
S75	1038	charge adj pump near1 capacitor .	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON ,	2005/11/06 19:00
S76	21190	capacitor adj "as" (charge adj pump)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/06 18:59
S77 i	. 0	"capacitor as charge pump"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/06 18:59
S78	0	capacitor adj "as" adj (charge adj pump)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/06 18:59
S79	369	charge adj pump near1 capacitor.ti, ab,clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/06 19:02
S80	48	charge adj pump near1 capacitor.ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/06 19:02
S81	1087	charge adj pump and bias near2 substrate	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/09 15:04
S82	3	charge adj pump and bias near2 substrate and spst adj switch	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/09 15:12
S83	0	reczek.in. and charge adj pump.ti, ab,clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/09 15:12

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S84	5	"712523".ap.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/09 15:31
S85	1680	(boost\$3 pump\$3) same (bias near6 substrate)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/22 17:04
S86	5	"712523".ap.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/11/10 15:35
. S87		"712523".ap	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/03 16:36
\$ \$88	. 2	("4798974").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/03 20:35
S89	6	("5386135").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/03 22:26
S90	3238	((327/534) or (327/535) or (327/536) or (327/537) or (257/299) or (257/901)).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/03 22:27
<b>S91</b>	4	S90 and (start-up power-up powering) and (body-bias\$3 body adj bias\$3) and switch\$3 and CMOS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/22 17:03
S92	3286	((257/299) or (257/901) or (327/534) or (327/535) or (327/536) or (327/537)).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/22 17:03
S93	4	S92 and (start-up power-up powering) and (body-bias\$3 body adj bias\$3) and switch\$3 and CMOS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON'	2006/08/22 17:03

S94	331	(boost\$3 pump\$3) same (bias near6 substrate) and S92	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/22 17:04
S95	5	"712523".ap.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/03/04 18:00
S96	24	(stabilization stabilizing stability) near4 threshold and cmos and sub\$1micron	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/04 18:02
S97 .	18.	(stabilization stabilizing stability) near4 threshold and cmos and sub\$1micron and @ad<"20031113"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR .	ON	2007/03/04 18:09
S98	10	(stabilization stabilizing stability) near4 threshold and cmos and sub\$1micron and @ad<"20031113" and switch\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/04 18:08
S99	1622	spdt adj switch	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/04 18:09
S10 .	0	S98 and S99	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON .	2007/03/04 18:09
S10 1	2218	(stabilization stabilizing stability) near4 threshold and @ad<"20031113"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/04 18:10
S10 .2		S99 and S101	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/04 18:12
S10 3	0.	spdt adj swtich and (cmos cmosfet)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/04 18:13

S10 4	181	spdt adj switch and (cmos cmosfet)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/04 18:15
S10 5	125	(single adj pole adj double adj throw spdt) adj switch and (cmos cmosfet) and substrate	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/04 18:16
S10 6	92	(single adj pole adj double adj throw spdt) adj switch and (cmos cmosfet) and substrate and @ad<"20031113"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB		ON	2007/03/04 18:21
S10 7 1	0.	(single adj pole adj double adj throw spdt) adj switch and (cmos cmosfet) and substrate and @ad<"20031113" and (stability stabilizing stabilize stabilization) near4 threshold	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/04 18:24
S10 8	2	(single adj pole adj double adj throw spdt) adj switch and (cmos cmosfet) and substrate and @ad<"20031113" and (stability stabilizing stabilize stabilization feed\$1back) near4 threshold	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/04 18:25